pbs

Examiner Signature

PTO/SB/08A (10-01)

Approved for use through 10/31/2002, CMB 0651-0031
Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE cliection of information unless it contains a valid OMB control number,

Substitute for form 1449A/PTO		Complete if Known				
		Application / Conf. No.	Unknown /	0		
INFORMATION DISCLOSURE				Filing Date	February 28, 2002	ā.
STATEMENT BY APPLICANT (use as many sheets as necessary)				First Named Inventor	Stephen M. Trimberger	S.
			LIOAITI	Art Unit	Unknown 2133	Ü,
			cessary)	Examiner Name	-Unknown-	23
Sheet	1	of	2	Attorney Docket Number	X-874 US	87

			U.S. PATENT	DOCUMENTS	
ekaminer nitate	Cite No.'	Document Number Number - Kind Code (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
Will		US- 5,434,514	07-18-95	Cliff et al.	
I N.Y		US- 6,167,558	12-26-00	Trimberger	
1////		US- 6,201,404 B1	03-13-01	Reddy et al.	
i P	i	US-			
i –		US-			
i		US-			
	i	US-			
		US-	·		
i	<u> </u>	US-			
· ·	i	US-			
i		US-			_
		US-			
,		US-			
i		US-			
		US-			
•		US-			
1		US-			
1	ļ	US-			
	i	US-			
i	i	US-			

		FORE	GN PATENT DOC	UMENTS		
Examiner Cite No.'	Cite	Foreign Patent Document	Publication Date	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages	Ι,,
	Country Code ^a -Number ⁴ -Kind Code ^a (<i>if known</i>)	MM-DD-YYYY	Applicant of Glob Document	or Relevant Figures Appear		
						#
	··········					-
						-
						-

*EXAMINER: Initial if reference considered, whether of not creation is in conformance with MPEP 609. Draw line through station if no inconformance and not considered. Include copy of this form with next communication to applicant.

Applicant's unique citation designation number (optionally. See Kinds of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. * Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). *For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. * Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. * Applicant is to place a check mark here if English language Translation is attached.

Date

Considered

Burden Hour Statement: This form is estimated to take 2.0 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, Patent and Trademark Office, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Assistant Commissioner for Patents, Washington, DC 20231.

PTO/SB/08A (10-01)
Approved for use through 10/31/2002. OMB 0651-0031
Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

Under the Panerwork Reduction Act of 1995, no page				Con	nplete if Known	
Substitute for form 1449A/PTO		Application / Conf. No.	Unknown /			
INFORMATION DISCLOSURE				Filing Date	February 28, 2002	
STATEMENT BY APPLICANT					Stephen M. Trimberger	SOC.
STATEMENT DI AFFEIGANT				Art Unit	Unknown	٥٥
(use as many sheets as necessary)			essary)	Examiner Name	Unknown	2
Sheet	2	of	2	Attorney Docket Number	X-874 US	5

		OTHER - NON PATENT LITERATURE DOCUMENTS	
Examiner hitials	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-Issue number(s), publisher, city and/or country where published.	Τ²
Ju	1	W. Bruce Culbertson, Rick Amerson, Richard J. Carter, Philip Kuekes, Greg Snider; "Defect Tolerance on the Teramac Custom Computer"; The 5th Annual IEEE Symposium on Field-Programmable Custom Computing Machines; April 16-17, 1997; pp. 116-123.	
	/	Fran Hanchek; Shantanu Dutt; "Node-Covering Based Defect and Fault Tolerance Methods for Increased Yield in FPGAs"; The Proceedings of the Ninth International Conference on VLSI Design; January 1996.	
M		Xilinx, Inc.; Virtex-II Platform FPGA Handbook'; published December 6, 2000; available from Xilinx, Inc., 2100 Logic Drive, San Jose, California 95124; pp. 33-75.	
7			
E.	!		
	1		
Exami Signat		Date Considered 932001	

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include/copy of this form with next communication to applicant.

Applicant's unique citation designation number 2 Applicant is to place a check mark here if English language Translation is attached.

Burden Hour Statement: This form is estimated to take 2.0 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, Patent and Trademark Office, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Assistant Commissioner for Patents, Washington, DC 20231.